(12) PATENT APPLICATION PUBLICATION

(19) INDIA

(22) Date of filing of Application :27/09/2021

(43) Publication Date : 05/11/2021

(71)Name of Applicant : 1)Dr.Suresh Kumar Pittala Address of Applicant :Associate Professor, Department of Electronics and Communication Engineering, R.V.R. & J.C. College of Engineering (Autonomous), Chandramoulipuram, Chowdavaram, Guntur, Andhra Pradesh, India. Pin Code: 522019 --2)Ms.B. Rama Sulochana 3)Dr.B.Rajani 4)Dr.Rajender Udutha 5)Dr.K.Gowrishankar 6)Mr.Rayudu Srinivas 7)Dr.Sushma Jaiswal 8)Mr.Tarun Jaiswal 9)Dr. Harish Chandra Mohanta 10)Mr.Syed Javeed Basha Name of Applicant : NA Address of Applicant : NA (72)Name of Inventor : 1)Dr.Suresh Kumar Pittala Address of Applicant :Associate Professor, Department of Electronics and Communication Engineering, R.V.R. & J.C. College of Engineering (Autonomous), Chandramoulipuram, Chowdavaram, Guntur, Andhra Pradesh, India. Pin Code: 522019 -2)Ms.B. Rama Sulochana :G02B0006420000, H05K0007200000, H01R0012880000, Address of Applicant : Assistant Professor, Department of ECE, Audisankara College of (51) International classification H01R0013660000, G02B0006360000 Engineering & Technology, Gudur, Andhra Pradesh, India. Pin Code:524101 ----(86) International Application ·PCT// 3)Dr.B.Rajani No :01/01/1900 Address of Applicant :Associate Professor, Department of Electrical And Electronics Filing Date (87) International Publication Engineering, Aditya College of Engineering & Technology. Surampalem, Andhra Pradesh, : NA No India. Pin Code: 533437 -(61) Patent of Addition to 4)Dr.Rajender Udutha :NA Application Number Address of Applicant : Assistant Professor, Department of Electronics and Communication :NA Filing Date Engineering, Vaageswari College of Engineering, Karimnagar, Telangana, India. Pin (62) Divisional to Application Code:505001 -:NA Number 5)Dr.K.Gowrishankar :NA Address of Applicant :Professor, Department of Electrical and Electronics Engineering, Sri Filing Date Manakula Vinayagar Engineering College, Madagadipet, Pondicherry, India. Pin Code:605107 6)Mr.Rayudu Srinivas Address of Applicant :Assistant Professor, Department of Electrical And Electronics Engineering, Aditya College of Engineering & Technology. Surampalem, Andhra Pradesh, India. Pin Code: 533437 7)Dr.Sushma Jaiswal Address of Applicant :Assistant Professor, Department of Computer Science & Information Technology (CSIT), Guru Ghasidas Vishwavidyalaya (A Central University), Koni, Bilaspur, Chhattisgarh, India, Pin Code: 495009 --8)Mr. Tarun Jaiswal Address of Applicant :Research Scholar, Department of Computer Application, National Institute of Technology (NITRR), Raipur, Chhattisgarh, India. Pin Code:492010 -9)Dr. Harish Chandra Mohanta Address of Applicant :Assistant Professor, Department of Electronics and Communication Engineering, Centurion University of Technology and Management, Odisha, India. Pin Code:752050 10)Mr.Syed Javeed Basha Address of Applicant : Assistant Professor, Department of ECE, Annamacharya Institute of Technology and Sciences, Rajampet, Andhra Pradesh, India. Pin Code: 516126 ---

(54) Title of the invention : A CONNECTOR MODULE FOR A VLSI CIRCUIT WITH A BATTERY PACK

(57) Abstract :

[034] The present invention discloses a connector module for a VLSI circuit with a battery pack. The system includes, but not limited to, a plug configured to VLSI circuit port module be communicatively coupled to the receptacle to be electrically/electronically connected to the receptacle; a coupling unit having a coupling section provided on the VLSI circuit having a shape protruding in a direction in which the external connector module for VLSI circuit is located and having coupling guide ribs, which is configured to guide a coupling protruding in a direction in which the external connector module for VLSI circuit solve and having coupling guide ribs, which is configured to guide a coupling protruding in a direction in which the external connector module for the VLSI circuit. Further, the coupling unit configured to be coupled with the coupling section, enabling the coupling section is movable within a predetermined distance range of the VLSI circuit, and a part, which is further configured to be fixed to the internal electronic / electrical device. Accompanied Drawing [FIG. 1]

No. of Pages : 22 No. of Claims : 9