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## (57) Abstract:

ABSTRACT A NOVEL METHOD & SYSTEM FOR DESIGNING VLSI CIRCUITRY TO OPTIMIZE THE INTEGRATED CIRCUIT OPERATION [031] The present invention discloses a system for designing VLSI circuitry. The system includes, but not limited to a top semiconductor layer constructed with a material with a higher resistivity and a higher transparency properties; a routing area having a plurality of component tiles positioned thereon; a means configured for reconfiguring the component tiles, enabling the design into a maximal component tiles and a maximal space tiles; and a processor for providing the instructions while designing and constructing the top semiconductor layer, the routing area, the component tiles and maximal space tiles. Accompanied Drawing [FIGS. 1 & 2]

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