

P.VENKATA MAHESH

Assistant Professor,  
EEE Department,  
RVR & JC College of Engineering, Chowdavaram,  
Guntur, Andhra Pradesh-522019.

**Phone:** 9491073317 & 9491073318 (O)

**Extension number:** 409

**Teaching Experience:** 7 years

**Educational qualifications:**



Program	Institution	Year
Ph. D. (Pursuing)	Annamalai University, Chidambaram, Tamilnadu.	Joined on DEC- 2018
M.Tech ( <i>Instrumentation and Control Systems</i> )	NIT Calicut, Kerala.	2014
B.Tech ( <i>Electrical and Electronics Engineering</i> )	Rajeev Gandhi Memorial College of Engineering and Technology, Nandyal, Kurnool, AP. (Affiliated to JNTUA)	2011

**Achievements:** GATE –All India 1487 rank in 2012.

– 4 Times GATE Qualified.

**Research Interests:** Machine Learning, Embedded Systems, Solar Systems and Control Systems.

**Memberships in Professional Societies:** Member of International Association of Engineers (IAENG).

**Research work / Research papers published:**

**Journals:**

1. P. Venkata Mahesh, Shyma Muhammed, Abraham T. Mathew, T. K. Sunil Kumar, “Model Approximation and Controller Synthesis for  $H_{\infty}$  Robust Control of Multiple Time Delay Transfer Functions”, *International Review of Automatic Control (I.R.E.A.CO.)*, Vol. 9, N. 1, ISSN 1974-6059. (SCOPUS)
2. P. Venkata Mahesh. S. Muhammed. Dr. SunilKuar T.K., “A New Approach for Approximate Modeling and Controller Design of SISO Multiple Time Delay System”, *IJRITCC*, vol. 2, no. 5, pp. 1230 - 1234, May 2014.

**Workshops /Seminars / Courses Participated:**

- Participated in One day seminar on “Deregulated power systems” organised by V.R.Siddhartha Engineering College on 27 Feb, 2016.
- Participated in TEQIP II Sponsored workshop on “Operation and Control of Wind -driven Generators” during 15 - 16, April 2016 at NIT Trichy, Tamilnadu.
- Participated in Workshop on “Free and Open Source Software in Teaching and Learning” during 4 - 5, March 2017 at NIT Warangal.

- Participated in short term course on “Artificial Neural Network & Fuzzy Logic through ICT”, from 24-28, April 2017 at RVR & JC College of Engineering.
- Participated in FDP conducted by Math Works Training Services “Embedded Coder for Production Code Generation” during 26 – 27, Feb 2018 at KL University.
- Participated in FDP conducted by Math Works Training Services “Generating HDL Code from Simulink” on 28 Feb 2018 at KL University.
- Participated in FDP101X on “Foundation Program in ICT for Education”, organized by IIT, Bombay at (Remote Centre) RVR & JCCE during 13 Sep - 27 Oct, 2018. (online course)
- Participated FDP 201X on “Pedagogy for online & Blended Teaching Learning Process”, organized by IIT, Bombay at (Remote Centre) RVR & JCCE during 30 Oct - 7 Jan, 2019. (online course)
- Participated in workshop on “Problem Identification, Research Methodology & Academic Writing”, organized by Kallam Haranadhareddy Institute of Technology during 28-29 Jan, 2019.
- Participated in workshop on “Advanced Process Control & Instrumentation System”, conducted by NITTTR, Kolkata, organized by RVRJCCE remote centre, during 7 - 11 Jan, 2019.
- Participated in “One Day Workshop on Arduino”, at IIT Bombay Remote center, R.V.R. & J. C. College of Engineering, Chowdavaram, Guntur on 8 Feb 2020.
- Participated in Two-Days webinar (SNU-WEBTECH SERIES-I) on “Machine and Deep Learning with MATLAB” organized by Sister Nivedita University, Kolkata, West Bengal during 3<sup>rd</sup> - 4<sup>th</sup> July, 2020.
- Participated in webinar on “Online Tools for Researchers” organised by Department of EEE, Albertain Institute of Science and Technology, Kalamassery, Kochi, Kerala on 7<sup>th</sup> July 2020.
- Participated in Five day Faculty Development Program on “Recent Trends and Developments in Electrical Power Engineering” organised by Chadalawada Ramanamma Engineering College, Tirupathi from 21<sup>st</sup> -25<sup>th</sup> June 2021.
- Participated in Three days online Faculty Development Program on “Written Communication for Digital Teaching, Administration & Research” organised by Dr. Babasaheb Ambedkar Technological University, Lonere Maharashtra from 22<sup>nd</sup> -24<sup>th</sup> March 2021.
- Participated in Webinar on “DFIG Based Wind Emulator using WAVECT” by WAVECT on 2<sup>nd</sup> June 2021.
- Participated in Webinar on “FPGA Controller for Electrical Engineering” by WAVECT on 19<sup>th</sup> May 2021.

**ORCID:** 0000-0002-7639-7601 (<https://orcid.org/0000-0002-7639-7601>)

**Scopus ID:** 57189247300 (<https://www.scopus.com/authid/detail.uri?authorId=57189247300>)

**Google Scholar ID:** PP6UjAgAAAAJ (<https://scholar.google.com/citations?user=PP6UjAgAAAAJ&hl=en>)

**Research Gate:** <https://www.researchgate.net/profile/Venkata-Mahesh>

**Publons:** <https://publons.com/researcher/4125631/p-venkata-mahesh/>

**Vidwan ID:** 187848 (<https://vidwan.inflibnet.ac.in/profile/187848>)